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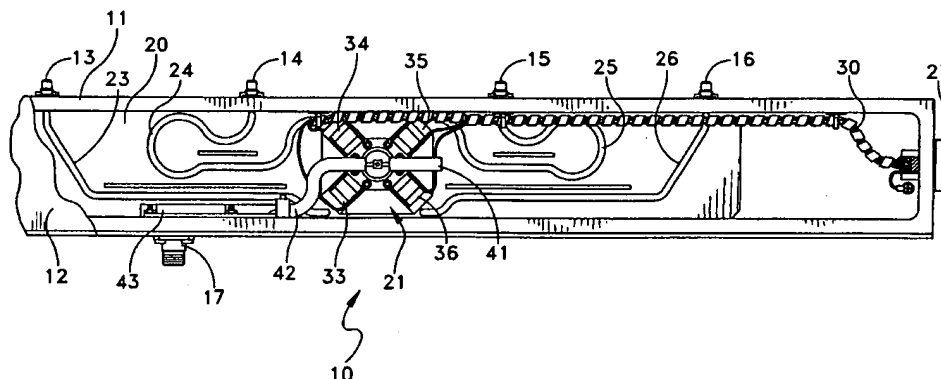
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(54) Title: RF POWER COMBINER CIRCUIT



(57) Abstract: An RF power combiner (10). A plurality of RF inputs (13, 14, 15, 16) connect to a power combiner switch assembly (21). RF switches (33, 34, 35, 36) connect each input to a common node (31). A single stub matching circuit (41, 42) connects between the common node (31) and an output node (32) such that an open-ended transmission line stub (41) extends from the output node (32). An RF output connector feeds an RF output connection from the output node (32). The stub (41) of the L-match circuit has fixed length or variable length configurations. As a consequence, the impedances at the common (31) and output (32) nodes are more closely matched.

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Background of the Invention  
RF Power Combiner Circuit

Technical Field

This invention generally relates to RF communications  
5 and more specifically to an N-way combiner that  
facilitates the control of a transmitted RF signal.

Background Art

Wireless RF applications, particularly in the 800 to  
1000 MHz and 1900 to 2400 MHz ranges, have become wide  
10 spread in recent years. These are frequencies of choice  
for wireless telephones and similar devices. Particular  
effort has been directed to the development of the high-  
power RF transmitting facilities for such applications  
including wireless telephone repeaters.

15 Many of these applications include multiple  
amplifiers to provide an appropriate RF output power. For  
example, a 600 watt transmitting facility may include four  
150 watt transmitters operating in parallel, rather than a  
single 600 watt transmitter. Using lower powered  
20 amplifiers provides reliability through redundancy and in  
many cases reduces costs as the cost of several lower  
powered RF amplifiers may be less than the cost of a  
single high powered amplifier. Moreover, the use of lower  
powered amplifiers allows different sites to be configured  
25 at different power levels without requiring different  
amplifiers. For example, a single amplifier could be used  
to provide a 150 watt transmitting facility; two  
amplifiers, a 300 watt transmitting facility; etc.

However, a single, high powered transmitter is  
30 characterized by simplified impedance matching to an  
antenna or other RF load. Generally the impedance match  
remains essentially the same for a given frequency  
regardless of the power being transmitted. With parallel,  
identical, lower powered amplifiers, however, the problem  
35 becomes more difficult because the output impedance of the  
collective amplifiers will be  $Z_0/N$  where  $Z_0$  is the  
characteristic impedance of one amplifier and N is the  
number of amplifiers operating in parallel. Thus, the

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impedance at a common node for a four-amplifier transmitting facility will vary between 50 ohms and  $12\frac{1}{2}$  ohms depending upon the number of amplifiers operating in parallel. If the impedance is not well matched, VSWR and  
5 insertion losses increase.

A number of power dividers and combiners have been proposed for minimizing the effects of impedance mismatches. Generally in these systems a single RF source produces an RF signal that divides into equi-phase, equi-  
10 amplitude input signals to parallel amplifiers. The combiner section then recombines the four amplified outputs to produce the high powered RF output signal. One particular approach, known in the art as a Wilkinson circuit, uses transmission lines at a characteristic  
15 impedance to convey signals to different ports. The ports are tied through resistors to a common node. The transmission lines may be anywhere from a quarter wavelength ( $\lambda/4$ ) to a half wavelength ( $\lambda/2$ ) in length. In such systems, however, optimal performance occurs when all  
20 parallel paths are energized. Insertion losses when only one amplifier is operating can become 75% of the input. With these losses it can be seen, particularly if equal amplitudes and phases are not maintained, that significant heat will be generated. In systems using resistors, this  
25 heat can lead to circuit failure.

United States Letters Patent No. 4,893,093 (1990) to Cronauer et al. discloses a switched, power splitter in which a high frequency input signal is applied to a plurality of amplifiers. First transmission lines connect  
30 between the input and each of the amplifiers with each transmission line capable of being switched between a high level and a low level of impedance. A balanced resistor network is preferably coupled between the first transmission lines. Second transmission lines shunt  
35 across the first transmission lines and the impedance of each second transmission line can be altered to a predetermined percentage of the circuits input impedance. A control circuit switches the various transmission lines

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so that the impedance of the antenna remains balanced no matter how many of the first transmission lines are in the high impedance state.

United States Letters Patent No. 5,767,755 (1998) to Kim et al. discloses another embodiment of a power combiner with a plurality of transmission lines connecting a plurality of inputs to an output terminal. RF switches provide the selection of up to N channels as active channels. The electrical length from each RF switch to the output terminal is preferably one-half wavelength at a central frequency (i.e.,  $\lambda/2$  at  $f_0$ ). When a switch is on, the signal power applied to all of input terminals is combined at the output terminal. When the switch is off, the RF power incident to the switch is reflected and the transmission line connected between that switch and the output terminal appear as an open circuit. However, it does appear the output impedance at the combined circuit can vary over a range of 4:1.

United States Letters Patent No. 5,867,060 (1990) to Burkett, Jr. et al. discloses still another embodiment of a power combiner that will allow the selection of a number of amplifiers operating in parallel for driving a load having characteristic impedance. Each amplifier connects to a common node through a phasing line one half-wavelength at the characteristic impedance. A quarter wavelength transforming line then connects the common node to the load. This transforming line has an impedance that depends upon the number of circuits being energized simultaneously. Therefore it appears that in this system a wide range of mismatches can still occur.

United States Letters Patent No. 5,872,491 (1999) to Kim et al. disclose a Wilkinson-type power divider/combiner that has a selective switching capability. The switchable power divider/combiner includes N first switches connecting N input/output transmission lines to a common junction and N second switches connecting N isolation resistors coupled to the N input/output transmission lines to a common node. The

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activation of each pair of the first and second switches to a closed or opened switch position controls the operating mode. Optimal impedance matching is provided by adjusting the impedance values to provide optimal  
5 impedance matching in both N-way and (N-1)-way operating modes. While this system appears to optimize for a particular configuration in anticipation of a failure of one path, it does not appear readily adapted for providing optimal impedance if more than one channel becomes  
10 inactive.

Examination of each of the foregoing patents and other representative prior art indicates that each of the approaches is overly complex. Problems of heating, insertion losses and impedance mismatches continue to  
15 exist. What is needed is a power combiner that can provide good VSWR and insertion loss characteristics over a wide range of input powers.

#### Disclosure of Invention

Therefore it is an object of this invention to  
20 provide an RF power combiner that is simple to construct and cost effective.

Another object of this invention is to provide an RF power combiner that exhibits a low VSWR for a wide range of operating power.

25 Still another object of this invention is to provide an RF power combiner that exhibits low insertion losses for a wide range of operating power.

In accordance with one aspect of this invention, a power combiner provides a plurality of RF input  
30 connections and an RF output connection. Switched RF feed lines connect each RF input connection to a common node. An RF output conductor connects the RF output connection to an output node. An impedance matching network connects to the common and output nodes and includes capacitive  
35 stub means connected to the output node.

In accordance with another aspect of this invention, a power combiner can produce at an RF output connection the combined outputs of up to four RF inputs in response

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to remotely generated selection signals. A transmission line and RF input switch connect in series between each RF input connection and a common node with said RF input switch being proximate the common node. An RF transmission line connects an output node to the RF output connection. A single stub matching circuit connects to the common and output nodes thereby to establish an impedance transformation function between the common and output nodes.

10 Brief Description of the Drawings

The appended claims particularly point out and distinctly claim the subject matter of this invention. The various objects, advantages and novel features of this invention will be more fully apparent from a reading of the following detailed description in conjunction with the accompanying drawings in which like reference numerals refer to like parts, and in which:

FIG. 1 is a plan view of one embodiment of a power combiner constructed in accordance with this invention;

20 FIG. 2 is a perspective view of a power combiner switch assembly constructed in accordance with one embodiment of this invention;

FIG. 3 is an electrical schematic diagram of the power combiner switch assembly shown in FIGS. 1 and 2;

25 FIG. 4 is a Smith chart that depicts the operation of the power combiner shown in FIGS. 1 through 3;

FIG. 5 is a plan view of another embodiment of a power combiner constructed in accordance with this invention;

30 FIG. 6 is an enlarged plan view of a power combiner switch assembly constructed in accordance with this embodiment of the invention;

FIG. 7 is a perspective view of the power combiner switch assembly shown in FIGS. 5 and 6;

35 FIG. 8 is an electrical schematic of the power combiner switch assembly shown in FIGS. 5 through 7;

FIG. 9 is a Smith chart that depicts the operation of the embodiment shown in FIGS. 5 through 8;

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FIG. 10 is an electrical schematic that depicts one embodiment of a control for use with the power combiner switch assembly shown in FIGS. 5 through 8; and

FIG. 11 is another embodiment of a control useful with the power combiner switch assembly shown in FIGS. 5 through 8.

Best Mode for Carrying Out the Invention

FIG. 1 depicts one embodiment of this invention in the form of a power combiner 10 mounted in a chassis 11 typically enclosed by a cover 12 that is partially shown. The chassis 11 carries four RF input connections 13, 14, 15 and 16. One or more RF transmitters, up to a maximum of four RF transmitters in this particular embodiment, connect to different ones of the RF input connections 13 through 16. The chassis 11 also carries an RF output connection 17.

A PC board 20 mounts in the chassis 11 and carries a number of components that are used in the power combiner 10 and particularly in a power combiner switch assembly 21. The printed circuit board includes transmission lines 23, 24, 25, and 26 formed as microstrips, or as like transmission lines, having a characteristic impedance (e.g.,  $Z_0 = 50$  ohms). Each of these transmission lines has an equal length to prevent any phase errors in the signals arriving at the power combiner switch assembly 21. A terminal block 27 provides a connection to an external control mechanism that can remotely select which of a plurality of RF switches in the power combiner switch 21 will be conductive and which will be non-conductive. A cable 30 conveys the selection signals to the solenoids of individual RF switches at the power combiner switch assembly 21.

Referring to FIGS. 1, 2 and 3, the power combiner switch assembly 21 in this particular embodiment includes a common node 31 and an output node 32. RF input switches 33 through 36 are equiangularly spaced about an axis through the common and output nodes 31 and 32 and connect individually to the transmission lines 23 through 26,

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respectively. Thus, a combination of a transmission line and its respective RF switch, such as the transmission line 23 and RF switch 33, form an RF feed line. All the RF feed lines attach to the common node 31.

5        FIG. 2 depicts a connection between one RF switch 33 and its respective transmission line 23 through a conductor 37. This connection is depicted for explanation purposes only. Any of a number of well known approaches will provide such a transmission line-to-switch  
10 connection.

For any specific application, one, two, three or all the RF switches 33 through 36 can be closed. Impedance matching for these widely divergent applications is provided by an impedance matching network between the  
15 common node 31 and the output node 32. This network is preferably in the form of a single stub matching circuit that includes a transmission line 40 of predetermined length between the common and output nodes 31 and 32 and an open-ended transmission line 41 that extends from the  
20 output node 32 and acts as a stub.

An output transmission line 42 connects between the output node 32 and an output coupler 43. This provides an output signal at the output RF connection 17.

Referring particularly to FIG. 3, it will be apparent  
25 that the impedance at the common node 31 can vary by 4:1. That is, assuming that each of the paths represented by the transmission lines 23 through 26 and the corresponding switches 33 through 36 have a characteristic impedance  $Z_0$  = 50 ohms, the input impedance will be 50 ohms if one of  
30 these switches 33 through 36 is conductive; 25 ohms if two of the RF switches 33 through 36 are conductive; 16.7 ohms if three of the RF switches 33 through 36 are conductive; and 12.5 ohms if all four of the RF switches 33 through 36 are conductive. Thus, the impedance mismatch with 50 ohms  
35 or other characteristic impedance, increases as the number of concurrently conductive RF switches increases.

The embodiment in FIGS. 1 through 3 assumes that applications with 3 or 4 active RF inputs require the most



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stringent impedance matching. The mean impedance for these two cases is 14 ohms. As previously indicated, impedance matching is provided a single stub matching circuit that includes the transmission line 40 and open circuit transmission line 41 that acts as a capacitive stub.

FIG. 4 is a Smith chart with a real axis 44. Points 45(1) through 45(4) represent the actual input impedance at the common node 31 when one through four of the RF input switches are energized. A point 46 represents the means impedance for operating conditions when either three or four RF input switches are closed. In this embodiment, point 46 is used to determine the lengths of the transmission lines 40 and 41 in the single stub matching circuit.

More specifically, arc 47 represents a phase delay introduced by the transmission line 40; arc 48, the phase shift introduced by the transmission line 41. The process for selecting specific values for these lengths is well known in the art. For this particular embodiment, the relative lengths are chosen so that the transmission line 40 introduces a phase delay of  $28^\circ$ ; the transmission line 41, a leading phase shift of  $52^\circ$ . As a result the single stub matching circuit provides an impedance match between the common and output nodes for this assumed impedance at the common node 31.

As will be apparent from FIG. 4, each of the initial points 45(1) through 45(4) indicate that the actual impedance at the common node 31 has a capacitive component that increases as the number of active RF input switches decreases. This capacitive component exists because there is a finite conductor length between the common node 31 and each RF input switch terminal proximate the common node 31. When an RF input switch is open, that finite conductive length acts as a capacitive stub. When only one RF input switch is closed, then there are three capacitive stubs; and the total capacitance is the sum of the values for each stub.

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In FIG. 4, end points 49(1) through 49(4) depict the effect of the single stub matching circuit. Point 49(4) is closely adjacent the end point for the nominal 14 ohm load. Arcs corresponding to arcs 47 and 48 are not shown with respect to this condition because they would superimpose on most of the arcs 47 and 48. In general, however, the use of the single stub matching circuit enhances the impedance match for each operating mode. As will be apparent from the Smith Chart and as has been demonstrated with actual measurements, the operational characteristics of the power combiner shown in FIGS. 1 through 3 are:

15

Number of / Conductive Inputs	VSWR	Insertion Loss
4	< 1.25:1	<0.5 dB
3	<1.25:1	<0.5 dB
2	<2:1	<0.8 dB
1	<4:1	<2.2 dB

FIGS. 5 through 8 depict an alternative embodiment that meets more stringent requirements that may be imposed on a power combiner such as shown in FIGS. 1 through 3, in certain applications. As previously stated, the embodiment shown in FIGS. 1 through 3 assumes that an open RF feed line, that is one in which the RF switch is non-conductive, acts as an infinite impedance at the common node 31. However, there is a conductive path of a finite length from the common node 31 to a terminal in the RF input switch that can act as a short stub. The embodiment in FIGS. 5 through 8 minimizes the length of such conductive paths. It also provides a tunable or variable length transmission line 41A.

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FIGS. 5 through 8 use like reference numerals to denote the same components as shown in FIGS. 1 through 3. If the elements are modified they are designated by the same reference numeral with a suffix "A".

5        FIGS. 5 through 8 depict a modified power combiner switch assembly 21A that receives signals from transmission lines 23 through 26. The difference in the RF input switches 33A through 36A is more clearly shown in the enlarged view of FIG. 6 that depicts the RF input  
10 switch 33A as including a base unit 60 and a stacked solenoid unit 61. The base unit 60 includes end walls 62 and 63 and side walls 64 and 65. Whereas in the embodiment of FIGS. 1 through 3 the RF input switch has a rectangular shape, the switch 33A has an octagonal shape  
15 formed by essentially chamfering the corners of the switch housings. For example, a chamfered surface 66 joins the end wall 62 and side wall 65. End walls, such as the end wall 63 on the switch 33A, can be positioned closer to an axis through the common and output nodes 31 and 32. Thus  
20 the length of any conductive path stub from the common node is shortened when a corresponding RF input switch is in an open-circuit or non-conductive state. In FIG. 8, this impedance is designated as  $Z_{st}(33A)$  for the RF switch 33A. Minimizing this length minimizes any error  
25 introduced by assuming that an open RF input switch produces an infinite impedance on the open circuit leg.

A further improvement occurs by modifying the construction of the stub 41 in FIGS. 1 through 3 to produce a stub 41A that has a variable length as is more

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specifically shown in FIGS. 5, 7 and 8. The power combiner switch assembly 21A includes a transmission line 42 which connects the output node 32 to a coupler 43 to the RF output connection 17. A single stub matching circuit includes a transmission line 40 between the common and output modes 31 and 32. However, in this embodiment, the stub 41A comprises two transmission lines 70 and 71 and an intermediate RF control switch 72 to provide a stub with a switchable or variable length. When the RF control switch 72 is not conductive, the stub 41A has an effective length corresponding to the length of the transmission line 70 plus any conductive path in the RF control switch. When the RF control switch 72 is conductive, the stub 41A has an effective length equal to the sum of the length of all the components including the first and second transmission lines 70 and 71, the total conductive path in the RF control switch 72 and any intervening connectors.

The variable length stub 41A enables the definition of two sets of operating conditions. The first involves combinations of three or four simultaneously conductive switches or active inputs (i.e., "3-4 operating modes"); the second set, combinations of one or two active inputs (i.e., a "1-2 operating modes"). Using an analogous analysis to that shown with respect to FIG. 4, for one particular application, the transmission line 40 between the common node 31 and output node 32 was selected to introduce a lagging phase shift of  $28^\circ$  for all operating modes.

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In this embodiment establishing the definition of the 3-4 operating modes and 1-2 operating modes and enabling the effective length of the stub 41A to be varied permits an analysis based upon two mean impedance assumptions.

5 That is, it is assumed that the mean impedance is 14 ohms for the 3-4 operating modes and 35 ohms for the 1-2 operating modes. With these specific assumptions, further analysis leads to a requirement for a  $52^\circ$  phase shift during the 3-4 operating modes and a  $20^\circ$  phase shift for

10 the 1-2 operating modes. This enables the physical lengths for the transmission lines 70 and 71 to be determined.

A transmission line that introduces  $20^\circ$  of phase shift is physically very short at the typical operating

15 frequencies; it is less than 2 cm. at 900 MHz. The arrangement of the components in the combiner switch assembly 21A may preclude such close spacing for the RF control switch 72. Thus, in the embodiment of FIGS. 5 through 8, the transmission line 70 has a length that

20 provides a  $52^\circ$  phase shift for the 3-4 operating modes when the RF control switch 72 is open. The transmission line 71 is then selected so that the transmission lines 70 and 71 and the conductive length through the RF control switch have a combined or total length of  $200^\circ$ ; that is,

25 the length is the  $20^\circ$  length needed to match impedances in the 1-2 operating modes plus  $180^\circ$ . This increase over the  $20^\circ$  length is one-half wave length, so when the RF control switch 72 is closed the stub 41A has an effective length of  $20^\circ$ .

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Points 80(1) through 80(4) in the Smith chart of FIG. 9 represent the common node input impedance for one, two, three and four active simultaneous inputs for a power combiner as shown in FIGS. 5 through 8. The initial 5 points in FIG. 9 are generally closer to the horizontal or real axes of the Smith chart of FIG. 4. This improvement results because the errors in the assumptions introduced by the finite open-circuit stub lengths as represented by  $Z_{et}(33)$  in FIG. 8 are reduced in that embodiment.

10 An upward extending arc 82(4) from the point 80(4) represents an impedance change caused by introducing the selected  $28^\circ$  phase delay. It has the same shape and length as the arc 47 in FIG. 4. This an equal delay applied to all the initial points 80(1) through 80(4); 15 they are designated 82(1) through 82(4), respectively.

During the 1-2 operating modes, the RF control switch 72 shifts to a conductive state so the effective length of the stub 41A, including the length of the conductive paths through the transmission lines 70 and 71 and the RF 20 control switch 72, determines the capacitive correction. That is shown by segments 83(1) and 83(2) in FIG. 9 for the curves with initial points 80(1) and 80(2) that represent a  $20^\circ$  phase shift. For 3-4 operating modes, the RF control switch 72 in non-conductive state. The 25 effective length for the stub 41A provides a  $52^\circ$  phase shift represented by the arcs 84(3) and 84(4) related to the starting points 80(3) and 80(4). As a result, the impedance at the output node 32 is transformed by this

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single stub matching circuit to the four operating points 85(0) through 85(4), respectively.

Tests on a particular embodiment of a power combiner 10 with a power combiner switch assembly 21A as show in 5 FIGS. 5 through 8, provides the following results:

	Number of Conductive Inputs	VSWR	Insertion. Loss
	4	<1.25	<0.5dB
	3	<1.3	<0.5dB
10	2	<1.6	<0.5dB
	1	<1.7	<0.5dB

FIG. 10 depicts one embodiment of a control circuit 100 for establishing the conductive state of the RF control switch 72 associated with variable length stub 41A 15 in FIGS. 5 through 8. A switch selector 101 provides various signals through the cable 30 in FIG. 5 to the control circuit 100. These remotely generated selection signals are designated RF(1) through RF(4) signals. Each of the relays in FIG. 10 is designated by reference to its 20 solenoid so the RF(1) through RF(4) signals control the solenoids 33S through 36S, respectively.

Whenever one or more of the remote selection signals from the switch selector 101 is active, one or more diodes 102 in a DC power supply 103 taps that signal or those 25 signals to produce an unregulated voltage on a conductor 104. A conventional voltage regulator circuit 105 and filter circuit 106 produce a regulated power supply voltage on a conductor 107.

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Each of the solenoids 33S through 36S connects to a common return 110. A voltage sensor 111, shown as a simple resistor 112, generates a voltage  $V_s$  on the return conductor 110 that is proportional to the current in the  
5 return conductor 110. As will be apparent, this voltage will step to essentially four different levels depending upon the number of switches that are active.

A comparator circuit 113 receives the  $V_s$  signal on conductor 110 and an adjustable set point signal from a  
10 potentiometer 114 connected to the regulated power conductor 107. The comparator comprises an open-loop operational amplifier comparator circuit 115 to control the conduction of a driver 116 that includes a transistor 117. The set point 114 is selected so that the voltage  $V_{sp}$   
15  $> V_s$  whenever any one or any two of the RF(1) through RF(4) signals are active. Thus, during the 1-2 operating modes the comparator circuit 113 biases the transistor 117 to a conductive state so the driver 116 energizes the solenoid 72S associated with the RF control switch 72.  
20 When the signal rises above the set point, as during the 3-4 operating modes,  $V_{sp} < V_s$  so the operational amplifier 115 biases the driver 117 to a non-conductive state and shifts the RF control switch 72 to a non-conductive state so only the one transmission line 70 in FIGS. 5 through 8  
25 is included in the stub 41A.

As will be apparent, this control circuit 100 in FIG.  
10 is readily adapted for installation in the power combiner as shown in FIG. 5. Moreover, the control circuit does not require any independent power supply, all



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the power can be readily derived from the signals applied to the solenoids 33S through 36S. Such a circuit can be included within the chassis of the power combiner 10A without any need for shielding. However, shielding is easily achieved by spacing or by conventional shielding techniques.

FIG. 11 depicts an alternative, digitally implemented control circuit 120 that can be substituted for the circuit 100 in FIG. 10. As in the control circuit 100 in FIG. 10, the control circuit 120 includes a switch selector 101 that provides the various RF(1) through RF(4) signals through the cable 30 in FIG. 5 to the control circuit 120 and then to the solenoids 33S through 36S, respectively that connect to the common return conductor 110.

Whenever one or more of the RF(1) through RF(4) remote selection signals is active, one or more of the diodes 102 in a DC regulated power supply 103 generates an unregulated voltage on a conductor 104 for energizing the solenoid 72S for the RF control switch.

In this embodiment, a digitally implemented comparator circuit 123 responds to logic level signals indicated the state of each of the RF(1) through RF(4) selection signals. Specifically, voltage regulator circuits are attached to be energized by one of the RF selection signals. As shown in FIG. 11, voltage regulators 124A through 124D generate V(A) through V(D) signals in response to the RF(1) through RF(4) selection signals, respectively.

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Each of the V(A) through V(D) signals provides a logical input to one or more NAND gates 130 through 133. Each NAND gate monitors a different one of the possible sets of three of the V(A) through V(D) inputs.

5 Consequently, one or more NAND gates 130 through 133 will shift to an assertive state (i.e., a logic "TRUE" state or a state wherein the output of a NAND gate is "HIGH") only when during the 3-4 operating modes when either three or four RF selection signals are active. For the 1-2  
10 operating modes, none of the NAND gates 130 through 133 will be in an asserted state.

When none of the NAND gates 130 through 133 is asserted, NAND gates 134 and 135 and a NOR gate 135 bias the transistor 117 into a conductive state. This  
15 energizes the solenoid 72S. So for the 1-2 operating modes, the entire stub 41A provides impedance matching. For the 3-4 operating modes, the transistor is biased off, so the transmission line 70 in the stub 41A is determinative of the impedance matching. As will be  
20 apparent the comparator circuit 123 uses the NAND gates 130 through 133 to sense the number of RF input switches that are conductive so that the comparator circuit 123 can control the conductive state of the RF control switch 72.

The two basic embodiments of this invention provide  
25 improved performance for a power combiner. Specifically, the use of the single stub matching circuit for transforming the impedance at a common node and an output node provides VSWR and insertion loss characteristics that are improved over power combiners that do not incorporate

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such a structure. Depending upon the stringency of the requirements, a power combiner circuit constructed in accordance with this invention is capable of two embodiments in which a simpler embodiment provides one set  
5 of characteristics and another embodiment with a variable length stub satisfies even more stringent requirements.

This invention has also been disclosed with respect to specific embodiments. Both embodiments depict a single stub matching circuit with a transmission line between the  
10 common and output nodes that lies along an axis that is orthogonal to the axis the plane of which RF conductors leading to the power combiner switch assembly. The open-loop transmission line operating as a stub from the output node and the output RF conductor are shown in planes that  
15 are parallel to the planes of the input conductors. Other angular and other spatial relationships could be implemented. Specific configurations of RF input switches and control switches and other components of a power combiner have been disclosed. Many of these physical  
20 characteristics and other elements can also be varied without departing from the spirit and scope of this invention while attaining some or all of the benefits of this invention. Therefore it is the intent of the appended claims to cover all such variations as come  
25 within the true spirit and scope of this invention.

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Claims

1. A power combiner having a plurality of RF input connections and an RF output connection and comprising:
  - 5 A) a switched RF feed line from each RF input connection to a common node,
  - B) an RF output conductor having a first end connected to the RF output connection and a second end defining an output node, and
  - 10 C) an impedance matching network interconnecting said common and output nodes and including capacitive stub means connected to said output node.
2. A power combiner as recited in claim 1 wherein each  
15 said RF feed line includes:
  - i) a transmission line extending from one RF input connection, and
  - ii) an RF switch interconnecting said transmission line and said common node,
  - 20 each RF switch being proximate said common node.
3. A power combiner as recited in claim 2 wherein each said RF switch is a remotely operated single-pole switch and said power combiner additionally includes  
25 connections for enabling the independent operation of said RF switches.
4. A power combiner as recited in claim 2 wherein each of said transmission lines lies in a plane and wherein said impedance transformer positions common

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and output nodes along an axis orthogonal to the plane and said impedance matching stub is parallel to said plane.

- 5 5. A power combiner as recited in claim 4 wherein said impedance matching network includes a delay line intermediate said common and output nodes whereby said delay line and said stub means provide single stub matching between the impedances at said common and output nodes.
- 10 6. A power combiner as recited in claim 2 wherein said impedance matching network includes a delay line intermediate said common and output nodes whereby said delay line and said stub means provide single stub matching between the impedances at said common and output nodes.
- 15 7. A power combiner as recited in claim 2 wherein said stub means lies generally parallel to the plane.
8. A power combiner as recited in claim 1 wherein said stub means has a variable length.
- 20 9. A power combiner as recited in claim 8 wherein said stub means includes first and second transmission lines and an intermediate control switch whereby said single stub matching circuit operates with different impedance matching characteristics depending upon the conductive state of said control switch.
- 25 10. A power combiner as recited in claim 9 additionally comprising a circuit that establishes the conductive state of said control switch in response to the number of RF switches that are closed.

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11. A power combiner for producing at an RF output connection the sum of up to four RF inputs applied to RF input connections in response to remotely generated selection signals, said power combiner comprising:
- 5
- A) a common node,
  - B) a transmission line and RF input switch in series between each RF input connection and said common node with said RF input switch being proximate said common node,
  - 10 C) an output node,
  - D) an RF transmission line connecting said output node to the RF output connection, and
  - E) a single stub matching circuit connected to said common and output nodes thereby to establish an impedance transformation function between said common and output nodes.
  - 15
12. A power combiner as recited in claim 11 wherein said L-match impedance matching circuit includes a delay line between said common and output nodes and an open-circuit stub connected to said output node.
- 20
13. A power combiner as recited in claim 12 wherein said stub includes first and second transmission lines and an intermediate RF control switch whereby the conductive state of said RF control switch determines the effective length of said stub.
- 25
14. A power combiner as recited in claim 13 wherein the operation of said power combiner is characterized by a first operating mode when one or two of said RF

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- input switches are closed and a second operating mode with three or four of said RF input switches are closed and wherein said RF control switch has first and second effective conductor lengths in the non-conductive and conductive states respectively, the combined lengths of said first transmission line and switch conductor length in the non-conductive state being selected to provide an impedance match for one of said operating modes and the combined lengths of said first and second transmission lines and said switch conductor length in the conductive state being selected to provide an impedance match for the other of said operating modes.
15. A power combiner as recited in claim 13 for operating at a predetermined frequency wherein the effective length of said stub means when said RF control switch is conductive is the sum of the a physical length selected for one of the impedance matching conditions and a physical length corresponding to one-half wavelength at the predetermined frequency.
16. A power combiner as recited in claim 12 additionally comprising means for controlling the conductive state of said RF control switch in response to the number of said RF input switches that are simultaneously conductive.
17. A power combiner as recited in claim 16 wherein said controlling means for said RF control switch includes:

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- i) power input means for obtaining power from the remotely generated selection signals,
  - ii) means for sensing the number of RF input switches that are in the conductive state, and
  - iii) means for controlling the conductive state of said RF control switch in response to said sensing means.
18. A power combiner as recited in claim 17 wherein said sensing means includes an analog summing junction for summing the current to the closed RF input switches.
19. A power combiner as recited in claim 17 wherein said sensing means includes a digital gating circuit for determining the number of active RF input switches.
20. A power combiner as recited in claim 17 wherein said control means includes an unregulated supply that receives each of the selection signals in parallel and a regulated supply connected to said unregulated supply, said sensor and said controlling means.
21. In an RF power combiner with a plurality of RF input switches that respond to remotely generated selection signals and with a switchable device that is controlled in response to the number of active RF input switches, a control circuit comprising:
- A) input means for receiving all the selection signals and for receiving all the return signals from each of the RF input switches,



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- B) a sensing circuit connected to said input means for determining the number of RF input switches that are active,
- C) a comparator circuit responsive to said sensing circuit signal for generating an control output signal, and
- 5 D) a driver responsive to the control signal for shifting said control switch to a conductive state.
- 10 22. A control circuit for a power combiner as recited in claim 21 additionally comprising a diode for conveying one of the selection signals to a common junction thereby to provide input power for said control means.
- 15 23. A control circuit for a power combiner as recited in claim 22 additionally comprising a regulated power supply connected to said common junction to provide a regulated voltage to said sensing circuit and said comparator circuit, said drive receiving power from
- 20 said common junction.
24. A control circuit for a power combiner as recited in claim 23 wherein said comparator circuit includes means for altering the level of the threshold signal.
- 25 25. A control circuit for a power combiner as recited in claim 21 wherein said sensing circuit includes an analog summing sensor for measuring the current in the return signals and said comparator circuit includes means for comparing the signal from said sensing means and a threshold signal.

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26. A control circuit for a power combiner as recited in claim 21 wherein said sensing circuit includes a first gating array for logically combining signals representing the state of the RF selection signals and wherein said comparator circuit includes another gating array responsive to the signals from said first gating array.
- 5

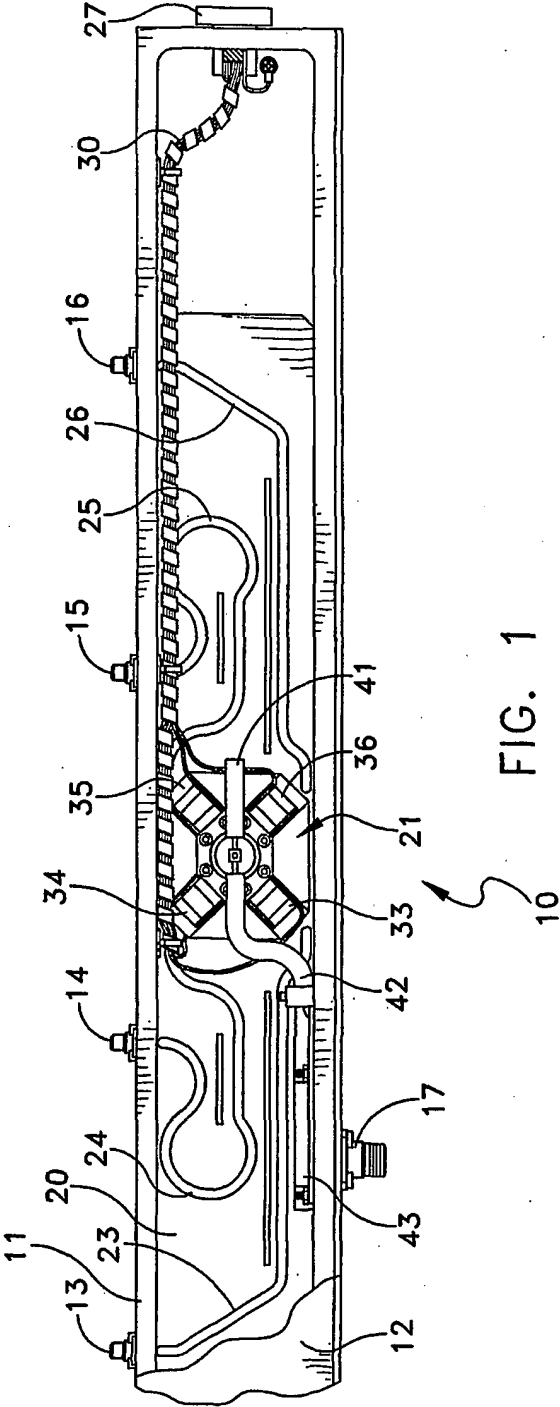


FIG. 1

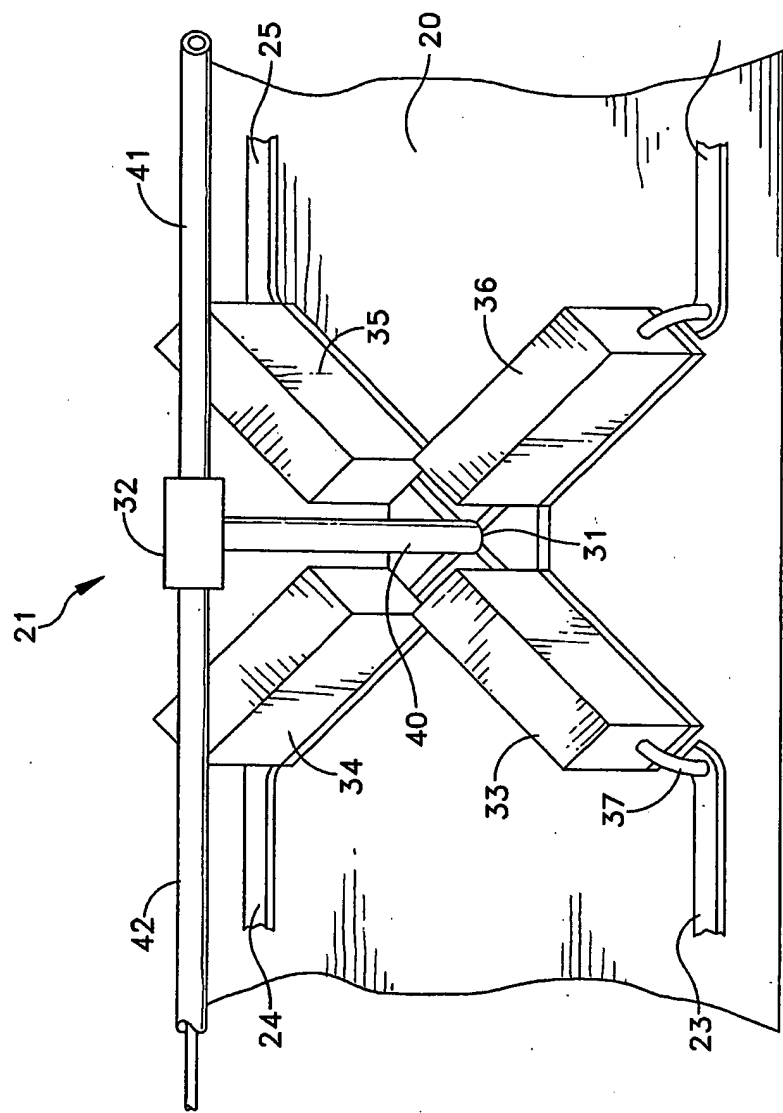


FIG. 2

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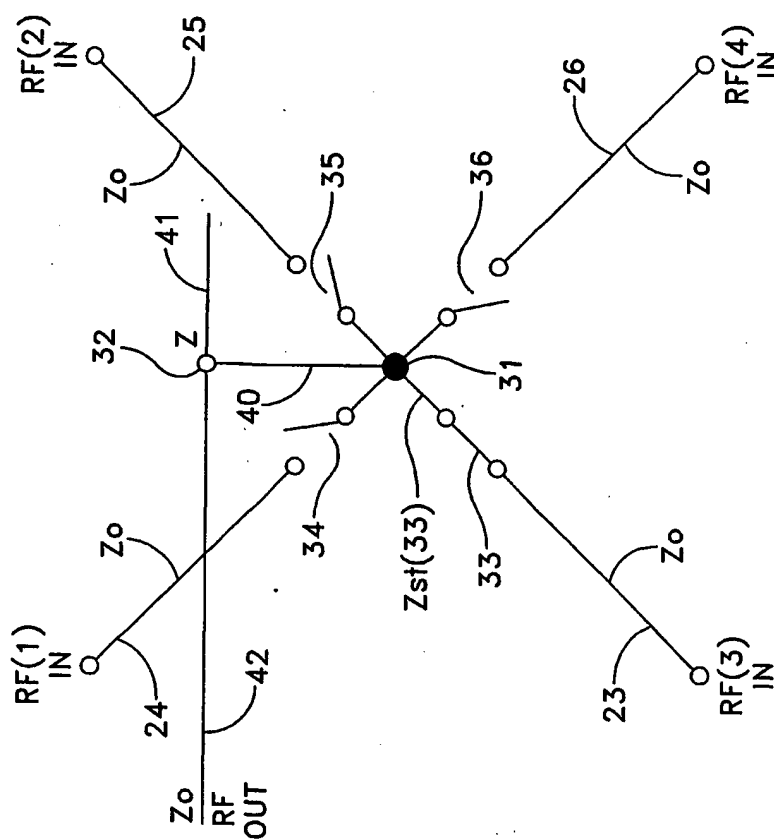
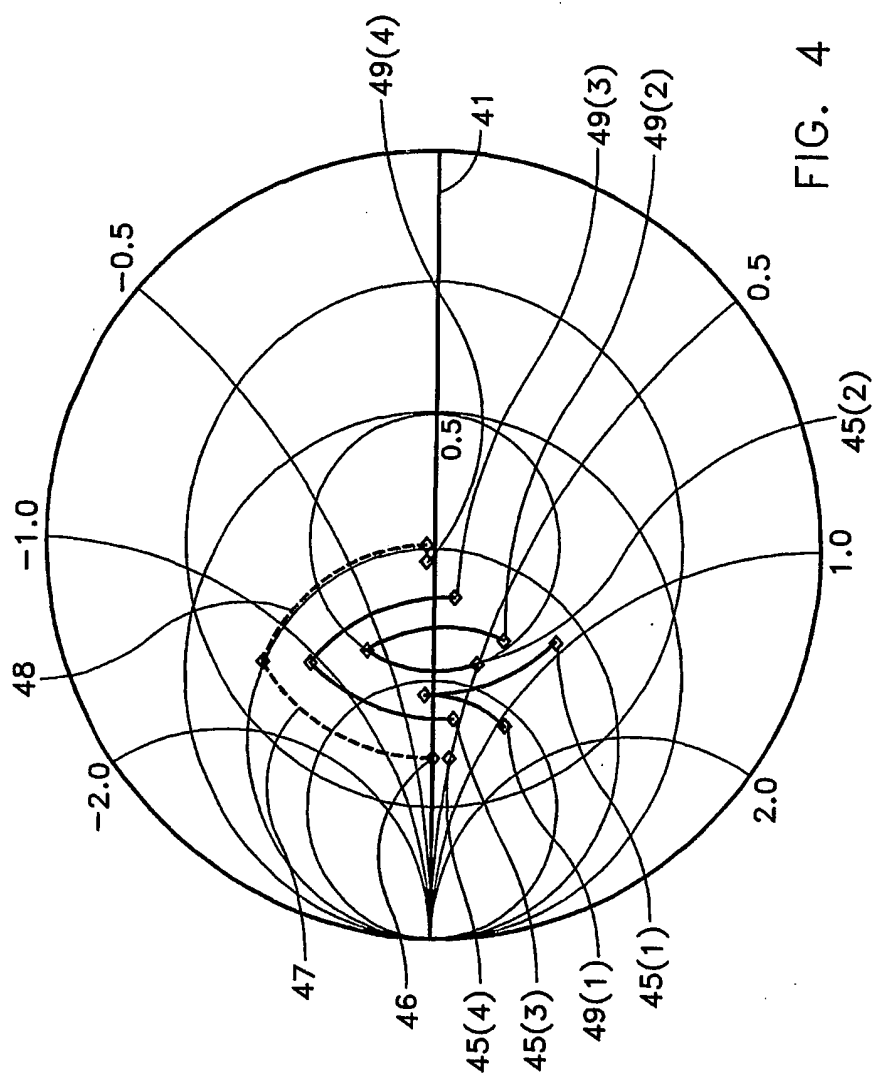


FIG. 3

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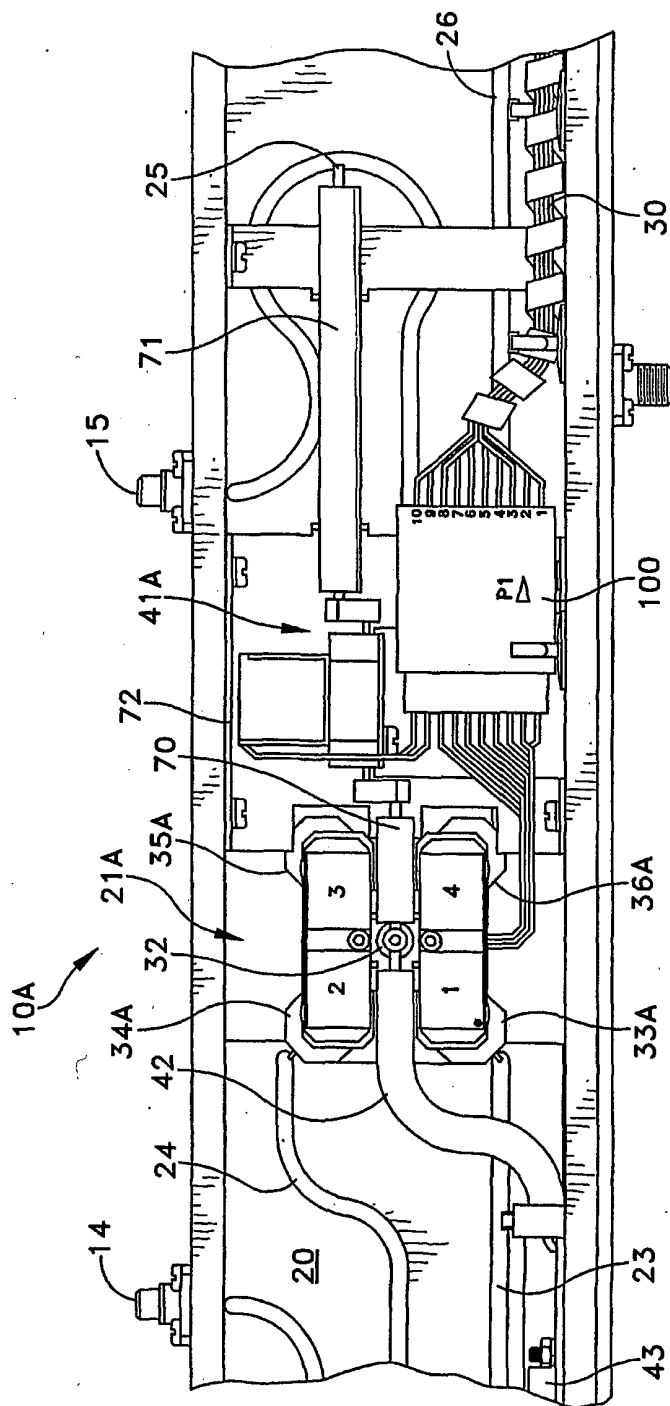
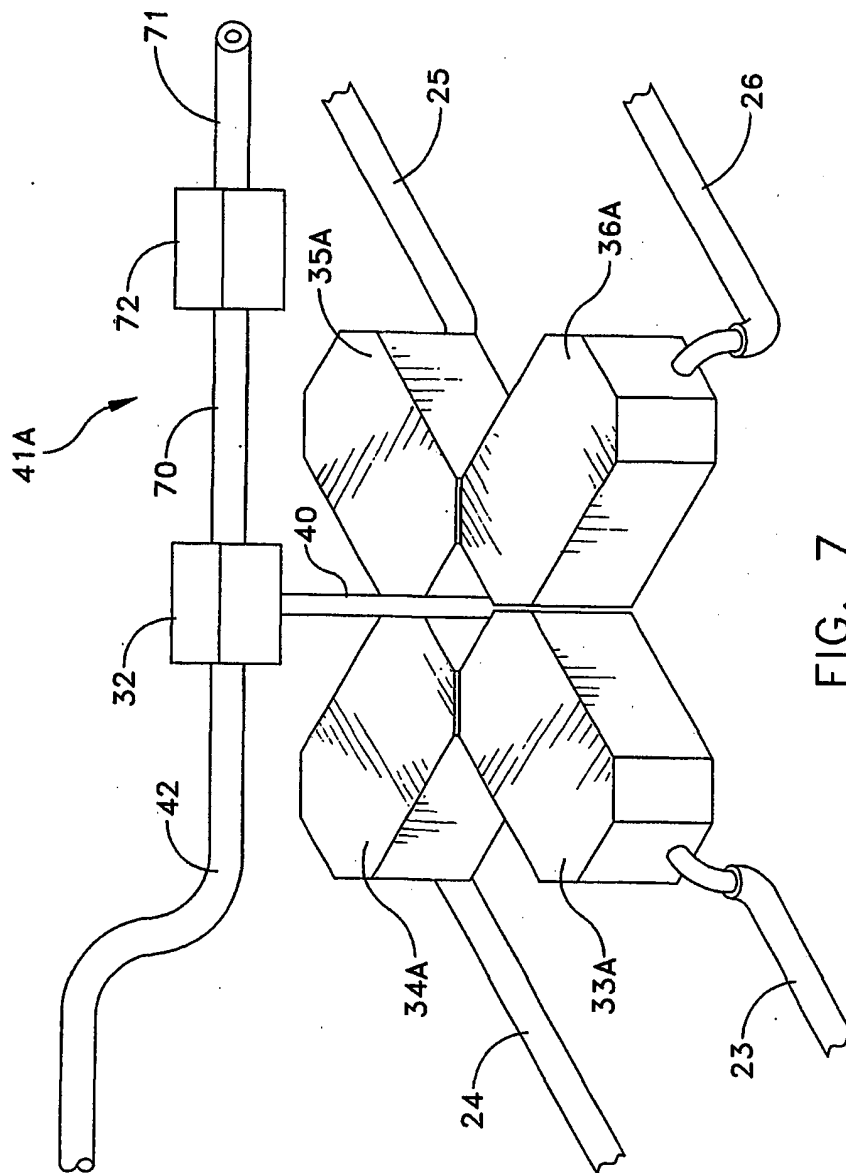


FIG. 5





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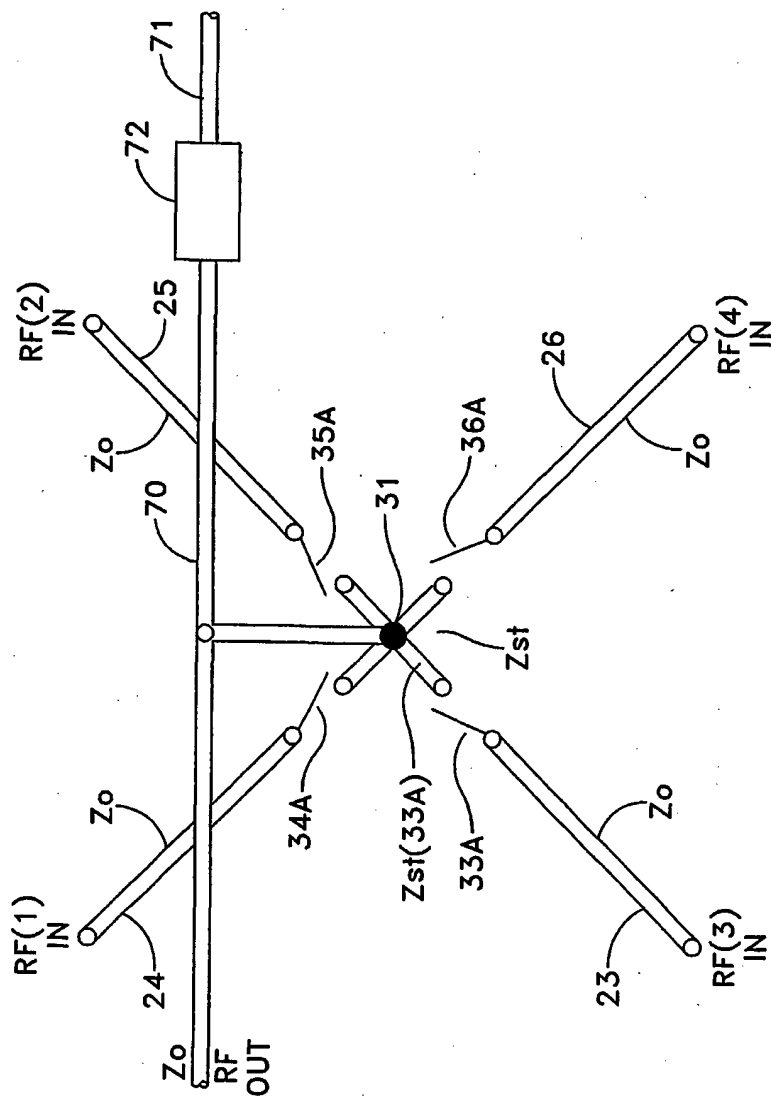


FIG. 8

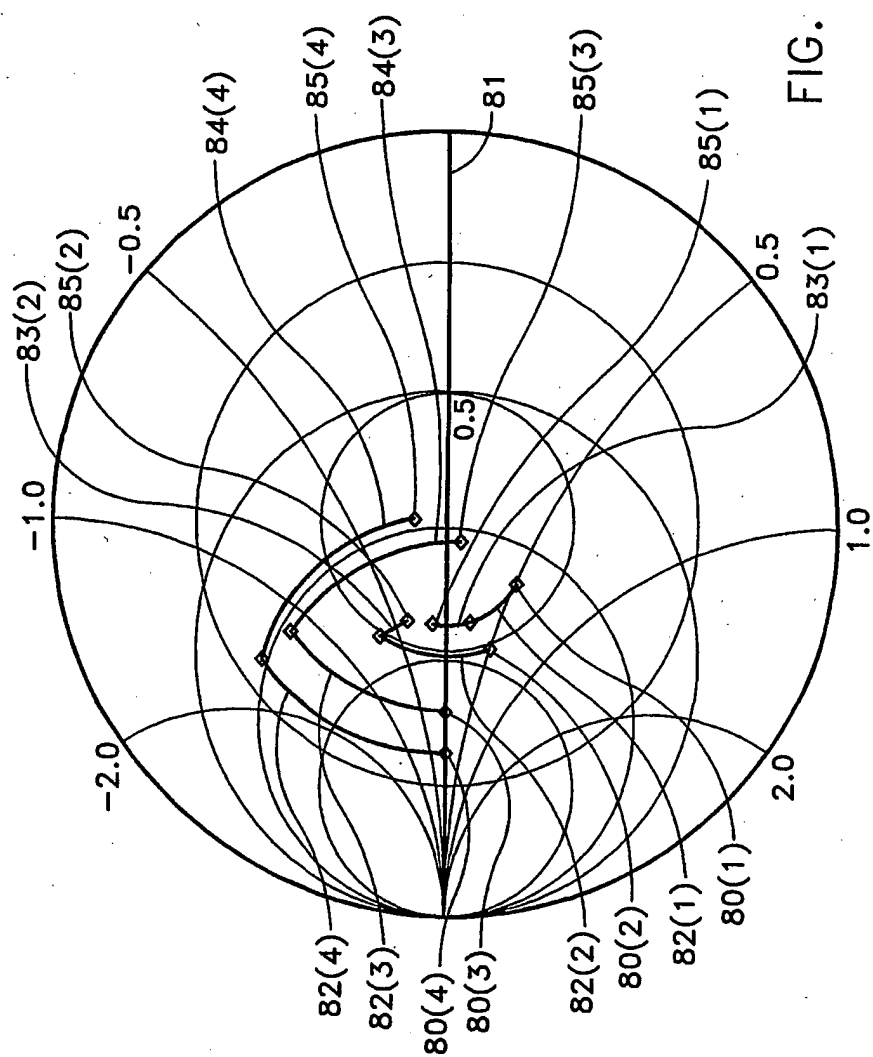
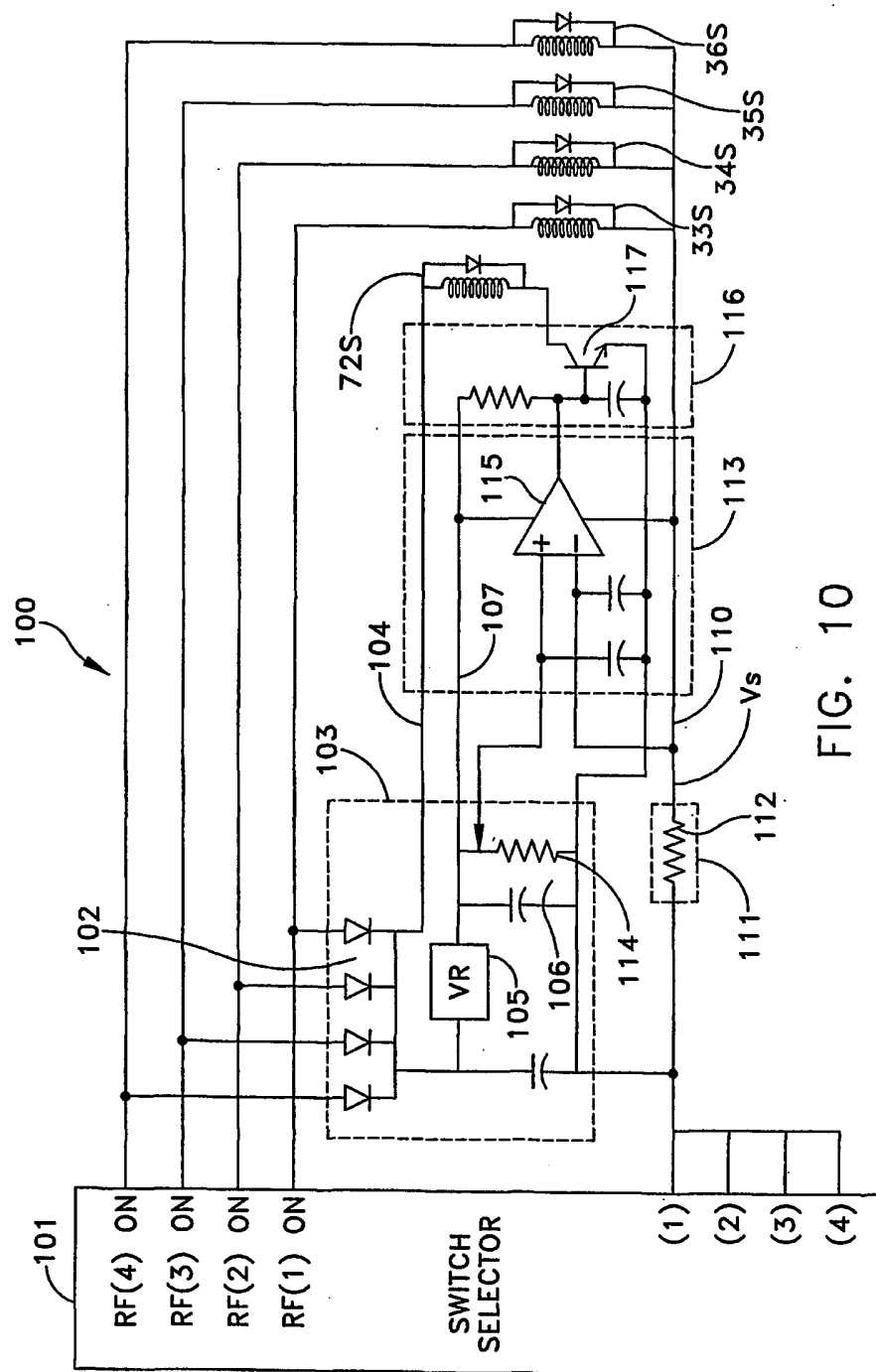
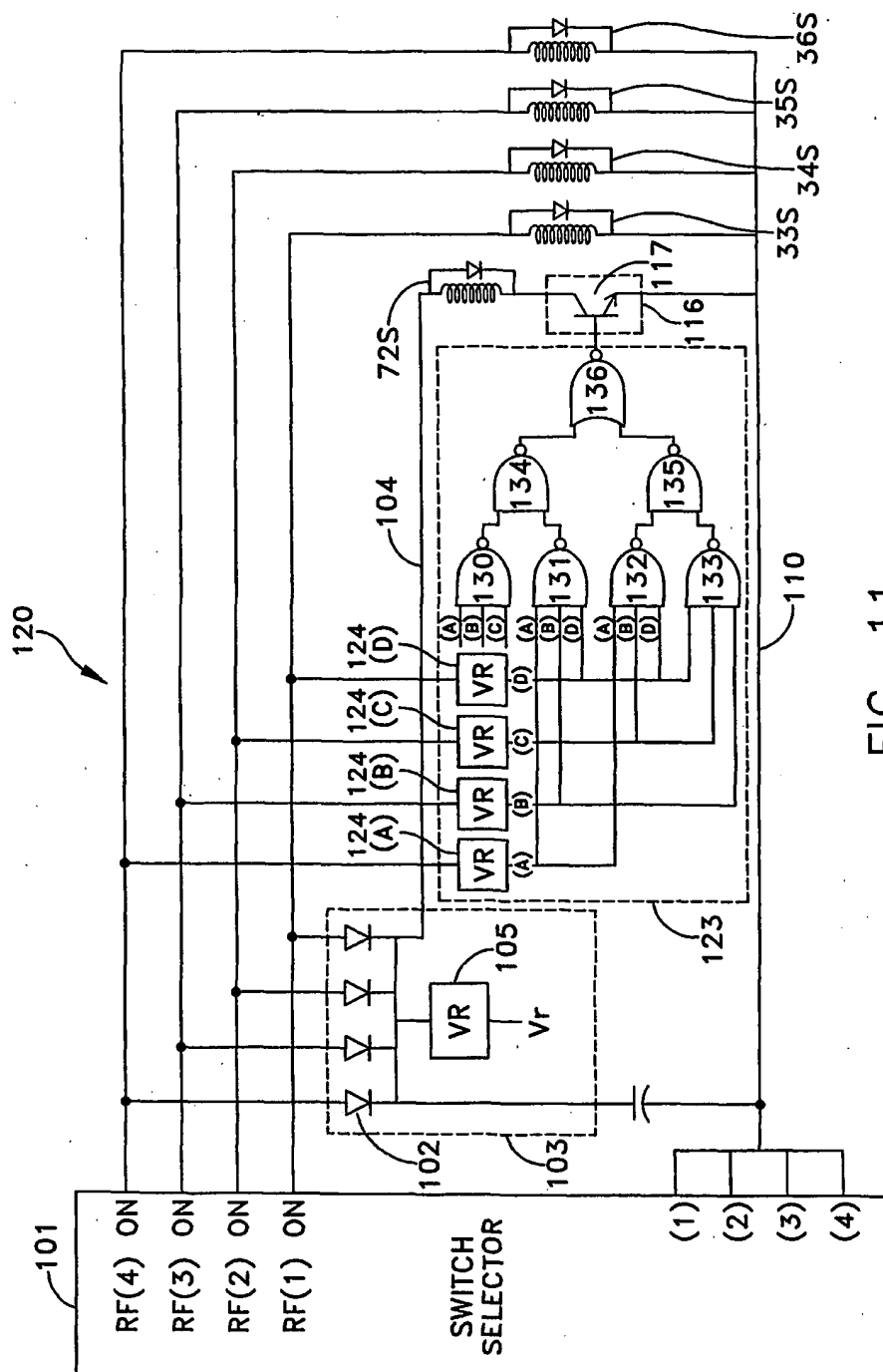


FIG. 9





## INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 01/03918

## A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H01P5/12

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H01P H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ

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X	PATENT ABSTRACTS OF JAPAN vol. 1998, no. 12, 31 October 1998 (1998-10-31) -& JP 10 200313 A (NEC CORP), 31 July 1998 (1998-07-31) abstract	1-3,8-16
A	----	21
A	US 4 646 038 A (WANAT) 24 February 1987 (1987-02-24) column 7, line 13 -column 8, line 51; figures 3-7	1,11,21
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☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

17 April 2001

Date of mailing of the international search report

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